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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
09/934,784	08/22/2001	Gary Gilliam	303.221US5	9291	
21186	7590 08/28/2002				
SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.			EXAMINER		
P.O. BOX 29 MINNEAPO	938 DLIS, MN 55402			KARLSEN, ERNEST F	
			ART UNIT	PAPER NUMBER	
			2829		

DATE MAILED: 08/28/2002

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)	•				
	09/934,784	GILLIAM, GARY					
Office Action Summary	Examiner	Art Unit					
1	Ernest F. Karlsen	2829					
The MAILING DATE of this communication appears on the cover sheet with the correspondence address Period for Reply							
A SHORTENED STATUTORY PERIOD FOR REPLY THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.13 after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a reply - If NO period for reply is specified above, the maximum statutory period v - Failure to reply within the set or extended period for reply will, by statute, - Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b). Status	36(a). In no event, however, may a reply be y within the statutory minimum of thirty (30) will apply and will expire SIX (6) MONTHS fi cause the application to become ABANDC	e timely filed days will be considered timely. rom the mailing date of this communic DNED (35 U.S.C. § 133).	ation.				
1) Responsive to communication(s) filed on 19 3	<u>lune 2002</u> .						
2a) ☐ This action is FINAL . 2b) ☑ Th	is action is non-final.						
3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.							
Disposition of Claims			9.				
4) Claim(s) 21-45 is/are pending in the application.							
4a) Of the above claim(s) is/are withdrawn from consideration.							
5) Claim(s) is/are allowed.							
6) Claim(s) 21-45 is/are rejected.							
7) Claim(s) is/are objected to.							
8) Claim(s) are subject to restriction and/o Application Papers	r election requirement.						
9) The specification is objected to by the Examine	r						
10) ☐ The drawing(s) filed on is/are: a) ☐ accept		xaminer.					
Applicant may not request that any objection to the							
11) The proposed drawing correction filed on is: a) approved b) disapproved by the Examiner.							
If approved, corrected drawings are required in reply to this Office action.							
12)☐ The oath or declaration is objected to by the Examiner.							
Priority under 35 U.S.C. §§ 119 and 120							
13) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).							
a) ☐ All b) ☐ Some * c) ☐ None of:							
1. Certified copies of the priority documents have been received.							
2. Certified copies of the priority documents have been received in Application No							
Copies of the certified copies of the prior application from the International Bu See the attached detailed Office action for a list	reau (PCT Rule 17.2(a)).						
14) Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).							
a) ☐ The translation of the foreign language provisional application has been received. 15)☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.							
Attachment(s)							
1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO-1449) Paper No(s) 2	5) Notice of Inform	nary (PTO-413) Paper No(s) nal Patent Application (PTO-152)	·				

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1. Claims 21-45 are rejected under 35 U.S.C. 112, first paragraph, as containing subject matter which was not described in the specification in such a way as to enable one skilled in the art to which it pertains, or with which it is most nearly connected, to make and/or use the invention. There is no original disclosure for an array of memory cells.

2. Claims 21-45 are rejected under 35 U.S.C. 112, first paragraph, as containing subject matter which was not described in the specification in such a way as to reasonably convey to one skilled in the relevant art that the inventor(s), at the time the application was filed, had possession of the claimed invention.

It is not clear how the disclosed apparatus forms a regulator. It is stated that he device is a regulator but no details are supplied to inform as to how regulation is to be performed.

- 3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 4. Claims 21-45 are rejected under 35 U.S.C. 103(a) as being unpatentable over McLaury in view of Bynum et al, Yim et al and Sawamura.

McLaury shows apparatus for regulating substrate bias. Bynum et al shows the concept of controlling the bias applied to a substrate by shunting or not shunting a diode in a line that applies a voltage to a substrate. Yim et al shows that plural diodes may be used in a line to tailor

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the applied voltage. Sawamura shows the equivalence of diodes and FETs connected as diodes as depicted in figures 5 and 6. It would have been obvious to one of ordinary skill in the art at the time of the invention to have adapted the controlling elements of Bynum et al to the apparatus of McLaury using plural diodes as suggested by Yin et al where the diodes are FETs connected as diodes as suggested by Sawamura because one skilled in the art would realize that such would make implementation of a voltage regulator easier using FET technology.

- 5. Claims 21-45 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention. It, is not clear what is meant by "array" or "voltage regulator".
- 6. The drawings are objected to under 37 CFR 1.83(a). The drawings must show every feature of the invention specified in the claims. Therefore, the array of memory cells must be shown or the feature(s) canceled from the claim(s). No new matter should be entered.

A proposed drawing correction or corrected drawings are required in reply to the Office action to avoid abandonment of the application. The objection to the drawings will not be held in abeyance.

Karlsen/ds

08/21/02

ERNEST KARLSEN